

IN THE SPECIFICATION

Please replace the paragraph beginning on page 10, line 15 with the following paragraph:

Referring to FIG. 6, a block diagram of a circuit 200 illustrating a preferred embodiment of the present invention is shown. The circuit 200 may be implemented as a product term input circuit of a programmable logic device. The circuit 200 may have an input that may receive the signal IT, an input that may receive the signal ITB, an input that may receive the signal C0, an input that may receive the signal C1, and an output that may present the signal PT_IN to one of a plurality of inputs of an AND plane 201. The AND plane 201 may be configured to generate product terms in response to the plurality of inputs. The signals C0 and C1 may be configuration bits of a programmable logic device. The circuit 200 may be configured to present the signal C0 or the signal C1 as the signal PT_IN in response to the signals IT and ITB. By selecting appropriate values for the signals C0 and C1, the circuit 200 may be configured to present the signal PT_IN as (i) a logic level that is independent of the signals IT and ITB or (ii) a signal that may change state similarly to either the signal IT or the signal ITB. An example operation of the circuit 200 may be summarized as in the following TABLE 2: